



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేక్షాగికి అం విజ్ఞాన సంస్థాన, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత్

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

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DEPARTMENT OF ELECTRONICS COMMUNICATION & INSTRUMENTATION ENGINEERING

5.8.1. Academic Research (20)

P13- CRITERIA 5

Name of the faculty	Number of publications in journals/conferences/Books/Book chapters																			
	2024-25 (CAY)			SCI/S COPU S	citatio ns	2023-24 (CAYm1)			SCI/SC OPUS	citatio ns	2022-23 (CAYm2)			SCI/S COPU S	citatio ns	2021-22 (CAYm3)			SCI/ SCO PUS	citatio ns
	J	C	B			J	C	B			J	C	B					J		
Dr. M. Sreelatha	0	0	0	0	4	0	0	0	0	4	0	0	0	0	4	0	0	0	0	4
Dr. K. Sivani	3	0	0	3	47	0	2	0	1	61	6	1	0	6	55	2	3	0	2	42
Dr. K. Venu Madhav	0	0	0	0	60	2	0	0	2	97	0	0	0	0	101	0	0	0	0	104
Smt. R. Nirmala Devi	0	0	0	0	4	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
Dr. O. Anjaneyulu	0	0	0	0	3	0	0	0	0	2	1	0	0	1	1	0	0	0	0	0
Dr. M. Raghu Ram	0	0	0	0	98	1	0	0	1	130	0	2	0	0	137	0	0	0	0	131
Sri B. Shashikanth	0	0	0	0	0	1	1	0	1	0	1	0	0	1	1	1	0	0	1	0
Dr. B. Jeevan	1	0	0	1	19	0	1	0	0	27	4	0	0	4	10	1	3	0	1	5
Dr. K. Srinivas	0	0	0	0	5	1	0	0	1	12	2	0	0	2	16	0	0	0	0	12
Sri B. Venu Maheswar	2	0	0	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Smt. B. Smitha	0	0	0	0	10	0	0	0	0	5	2	3	0	2	5	0	3	0	0	0
Smt. K. Shailaja	3	0	0	3	19	0	0	0	0	20	1	0	0	1	12	0	0	0	0	4
Sri G. Raju	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0
Sri B. Krishna Sundeeep	-	-	-	-	-	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Total (J, C & B)	9	0	0	9	279	5	5	0	6	358	18	6	0	18	342	5	9	0	5	302
Sum	9	0	0	9	297	10	0	0	6	358	24	0	0	18	342	14	0	0	5	302



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काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना, भारत

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

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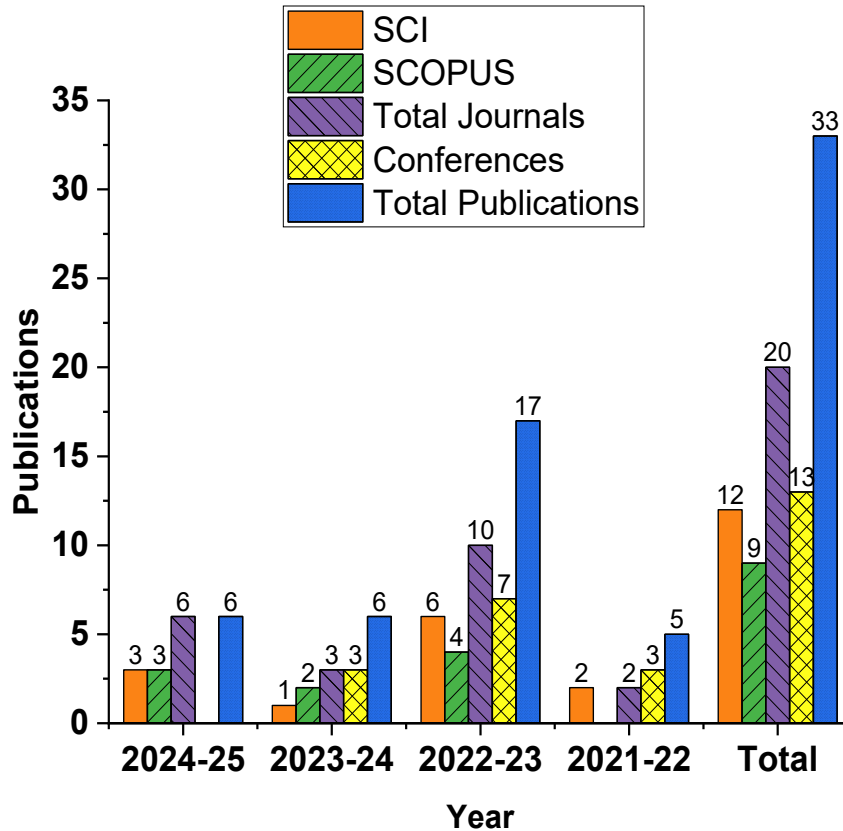
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DEPARTMENT OF ELECTRONICS COMMUNICATION & INSTRUMENTATION ENGG.

P13- CRITERIA 5

5.8.1. Academic Research (20)

Year	SCI	SCOPUS	TOTAL Journal Publications	Conference Publications	Total Publications
2024-25 (CAY)	03	05	08	-	08
2023-24 (CAYm1)	01	01	02	03	05
2022-23 (CAYm2)	08	05	13	03	16
2021-22 (CAYm3)	03	01	04	06	10
Total	15	12	27	12	39





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P13- CRITERIA 5

Dr. K. Sivani
<u>2024-25</u>
SCI
[1] J. Battini and S. Kosaraju, "An efficient single-stage carry select adder using excess-1 FinFET circuit in 22 nm technology," <i>Semicond Sci Technol</i> , vol. 39, no. 9, Sep. 2024, doi: 10.1088/1361-6641/ad6e15.
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[1] C. P. Kumar and K. Sivani, "Ultra Low Power and High Speed Electronic Circuits Using Double Gate Tunnel Field Effect Transistor," <i>Journal of The Institution of Engineers (India): Series B</i> , pp. 1–21, 2024. https://doi.org/10.1007/s40031-024-01122-y
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<u>2023-24</u>
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[1] B. Jeevan and K. Sivani, "A new 18nm FinFET-based Programmable Logic Array type Multiplexer for High-speed and Low-Power applications," in <i>2023 IEEE 20th India Council International Conference, INDICON 2023</i> , Institute of Electrical and Electronics Engineers Inc., 2023, pp. 1205–1210. doi: 10.1109/INDICON59947.2023.10440683.
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<u>2022-23</u>
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[1] B. Jeevan, K. Bikshalu, E. Hari krishna, and K. Sivani, "Design of 2-1 Multiplexer based high-speed, Two-Stage 90 nm Carry Select Adder for fast arithmetic units," <i>Microprocess Microsyst</i> , vol. 99, Jun. 2023, doi: 10.1016/j.micpro.2023.104846.
[2] J. Battini and S. Kosaraju, "Design of efficient 22 nm, 20-FinFET full adder for low-power and high-speed arithmetic units," <i>Silicon</i> , vol. 15, no. 2, pp. 993–1002, Jan. 2023, doi: 10.1007/s12633-022-02073-z.
[3] B. Jeevan and K. Sivani, "A 16 nm FinFET circuit with triple function as digital multiplexer, active-high and active-low output decoder for high-performance SRAM architecture," <i>Semicond Sci Technol</i> , vol. 37, no. 8, Aug. 2022, doi: 10.1088/1361-6641/ac77ae.

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[1] J. Battini, K. Bikshalu, and S. Kosaraju, "A 90 nm area and power efficient Carry Select Adder using 2-1 multiplexer based Excess-1 block," <i>Engineering Research Express</i> , Mar. 2023, doi: 10.1088/2631-8695/acc514.
[2] E. H. Krishna, K. Sivani, and K. A. Reddy, "OFDM Channel Estimation Along with Denoising Approach under Small SNR Environment using SSA," <i>Journal of Communications Software and Systems</i> , vol. 18, no. 1, pp. 28–35, 2022. DOI: https://doi.org/10.24138/jcomss.v18i1.1082
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[1] S. Khaliq and K. Sivani, "The role of EEG-based brain computer interface using machine learning techniques: a comparative study," in <i>2022 IEEE Industrial Electronics and Applications Conference (IEACon)</i> , 2022, pp. 128–133. DOI: 10.1109/IEACon55029.2022.9951740
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[1] B. Jeevan, P. Sahithi, P. Samskruthi, and K. Sivani, "Simulation and synthesis of UART through FPGA Zedboard for IoT applications," in <i>Proceedings - IEEE International Conference on Advances in Computing, Communication and Applied Informatics, ACCAI 2022</i> , Institute of Electrical and Electronics Engineers Inc., 2022. doi: 10.1109/ACCAI53970.2022.9752556.
[2] B. Jeevan and K. Sivani, "Design of 64-bit Signed Magnitude Comparator using FPGA for IoT applications," in <i>Proceedings - IEEE International Conference on Advances in Computing, Communication and Applied Informatics, ACCAI 2022</i> , Institute of Electrical and Electronics Engineers Inc., 2022. doi: 10.1109/ACCAI53970.2022.9752617.
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Dr. K. Venu Madhav
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Smt. R. Nirmala Devi
2021-22
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Dr. O. Anjaneyulu
2022-23
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[1] O. Anjaneyulu and C. V. K. Reddy, "A novel design of full adder cell for VLSI applications," <i>International Journal of Electronics</i> , vol. 110, no. 4, pp. 670–685, 2023. https://doi.org/10.1080/00207217.2022.2059819 .

Dr. M. Raghu Ram
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[1] M. Divija and R. Ram, "IoT based smart irrigation module for smart cultivation," in <i>2022 International Conference on Wireless Communications Signal Processing and Networking (WiSPNET)</i> , 2022, pp. 189–192. DOI: 10.1109/WiSPNET54241.2022.9767170
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Sri B. Shashikanth
2023-24
SCOPUS
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Dr. B. Jeevan
<u>2024-25</u>
SCI
[1] J. Battini and S. Kosaraju, "An efficient single-stage carry select adder using excess-1 FinFET circuit in 22 nm technology," <i>Semicond Sci Technol</i> , vol. 39, no. 9, Sep. 2024, doi: 10.1088/1361-6641/ad6e15.
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[3] B. Jeevan and K. Sivani, "A 16 nm FinFET circuit with triple function as digital multiplexer, active-high and active-low output decoder for high-performance SRAM architecture," <i>Semicond Sci Technol</i> , vol. 37, no. 8, Aug. 2022, doi: 10.1088/1361-6641/ac77ae.
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<i>Computing, Communication and Applied Informatics, ACCAI 2022</i> , Institute of Electrical and Electronics Engineers Inc., 2022. doi: 10.1109/ACCAI53970.2022.9752556.
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[3] B. Jeevan, P. Samskruthi, P. Sahithi, and K. Sivani, "Implementation of parallel multiplier based on Booth computing method using FPGA," in <i>Proceedings - IEEE International Conference on Advances in Computing, Communication and Applied Informatics, ACCAI 2022</i> , Institute of Electrical and Electronics Engineers Inc., 2022. doi: 10.1109/ACCAI53970.2022.9752479.

Dr. K. Srinivas
2023-24
SCOPUS
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2022-23
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Sri B. Venumaheswar Rao
2024-25
SCI
[1] V. R. Bondala and A. R. Komalla, "An efficient model for extracting respiratory and blood oxygen saturation data from photoplethysmogram signals by removing motion artifacts using heuristic-aided ensemble learning model," <i>Comput Biol Med</i> , vol. 180, p. 108911, 2024. https://doi.org/10.1016/j.combiomed.2024.108911
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Smt. B. Smitha
2022-23
SCI
[1] S. Sodagudi, S. Manda, B. Smitha, N. Chaitanya, M. A. Ahmed, and N. Deb, "EEG signal processing by feature extraction and classification based on biomedical deep learning architecture

	with wireless communication,” <i>Optik (Stuttg)</i> , vol. 270, p. 170037, 2022. https://doi.org/10.1016/j.ijleo.2022.170037
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2021-22	
Conferences	
[1]	B. Smitha and B. S. S. I. Dutt, “Orthogonal Wavelet Construction Method for Electrocardiographic Signal Denoising,” in <i>2022 10th International Conference on Emerging Trends in Engineering and Technology-Signal and Information Processing (ICETET-SIP-22)</i> , 2022, pp. 1–6. DOI: 10.1109/ICETET-SIP-2254415.2022.9791782
[2]	B. Smitha, V. S. Kumar, and B. S. Dutt, “Fetal ECG extraction algorithm based on single-channel abdominal signals,” in <i>AIP Conference Proceedings</i> , 2022. DOI:10.1063/5.0081766
[3]	B. Smitha, V. S. Kumar, and B. S. Dutt, “Cognitive radio subcarrier power allocation based on artificial physical optimization,” in <i>AIP Conference Proceedings</i> , 2022. https://doi.org/10.1063/5.0081764

Smt. K. Shailaja	
2024-25	
SCI	
[1]	S. Kotte and J. R. K. Kumar Dabbakuti, “Smart 6G Sensor Network Based Human Emotion Analysis by Machine Learning Architectures,” <i>Wirel Pers Commun</i> , pp. 1–17, 2024. DOI https://doi.org/10.1007/s11277-024-11188-y
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[1]	S. Kotte and J. R. K. K. Dabbakuti, “EEG Signal-Based Emotion Detection of Parkinson’s Patients for Classification and Feature Extraction by Deep Learning Architecture,” <i>SSRG International Journal of Electronics and Communication Engineering</i> , vol. 11, no. 10, pp. 267–276, Oct. 2024, doi: 10.14445/23488549/IJECE-V11I10P122.
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[1]	A. S. K. Reddy <i>et al.</i> , “Multi-modal fusion of deep transfer learning based COVID-19 diagnosis and classification using chest x-ray images,” <i>Multimed Tools Appl</i> , vol. 82, no. 8, pp. 12653–12677, 2023. DOI https://doi.org/10.1007/s11042-022-13739-6

Sri G. Raju	
2022-23	
SCI	
[1]	R. Ganna, S. Saxena, and G. S. Patel, “Design of Power, Area and Delay Optimized Direct Digital Synthesizer Using Modified 32-Bit Square Root Carry Select Adder,” <i>Journal of Circuits, Systems and Computers</i> , vol. 31, no. 17, p. 2250292, 2022. https://doi.org/10.1142/S0218126622502929

Sri B. Krishna Sundeeep
2022-23
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[1] K. S. Basavaraju, T. K. Kumar, K. A. Reddy, and K. R. K. Reddy, “Analysis of Vibroarthrographic signals for classification of knee disorders using Empirical Wavelet Transform based on Statistical measures,” in <i>2023 IEEE International Instrumentation and Measurement Technology Conference (I2MTC)</i> , 2023, pp. 1–6. doi: 10.1109/I2MTC53148.2023.10175958.